

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: PETER CONNLEY LANCASTER

DOCKET NO.: TUC920040006US1

SERIAL NO.:

FILED:

EXAMINER:

ART UNIT:

TITLE: EFFICIENT REAL-TIME ANALYSIS METHOD OF ERROR LOGS FOR
AUTONOMOUS SYSTEMS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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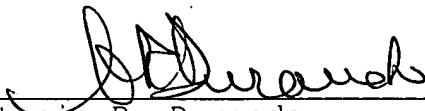
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Dear Sir:

In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, applicant wishes to make of record the items on attached PTO Form 1449. Copies of the items are provided in accordance with 37 CFR 1.98.

Applicant believes that none of the foregoing referenced material, alone or in combination, anticipates or renders obvious his invention.

Respectfully submitted,



Antonio R. Durando
Reg. No. 28,409

Date: 4/15/04

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	
Filing Date	
First Named Inventor	Peter C. Lancaster
Art Unit	
Examiner Name	
Attorney Docket Number	TUC920040006US1

Sheet 1 of 2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 2003/0070115 A1	04/10/2003	Nguyen et al.	
		US- 2003/0074607 A1	04/17/2003	Brundridge et al.	
		US- 3,704,363	11/28/1972	Salmassey et al.	
		US- 4,964,129	10/16/1990	Bowden, III et al.	
		US- 4,979,059	12/18/1990	Sengoku	
		US- 5,463,768	10/31/1995	Cuddihy et al.	
		US- 5,500,940	03/19/1996	Skeie	
		US- 6,148,415	11/14/2000	Kobayashi et al.	
		US- 6,345,322 B1	02/05/2002	Humphrey	
		US- 6,625,589 B1	09/23/2003	Varma et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0 517 403 A2	12/09/1992	Treu		
		JP 2002-215431 A	08/02/2002	Toshiba Corp.		
		JP 2002-312205 A	10/25/2002	Hitachi		
		JP 63-027940 A	02/05/1988	Mitsubishi		

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Considered

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	
		Filing Date	
		First Named Inventor	Peter C. Lancaster
		Art Unit	
		Examiner Name	
Sheet 2	of 2	Attorney Docket Number	TUC920040006US1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		IBM Technical Disclosure Bulletin, "Error Recording Architecture," July 1980, pp. 763-766	
		IBM Technical Disclosure Bulletin, "Method for Improved Diagnostic Fault Isolation of Intermittent Multiple Bit Failures in an Interleaved Memory Design," 3/1987, p 4645-4646	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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